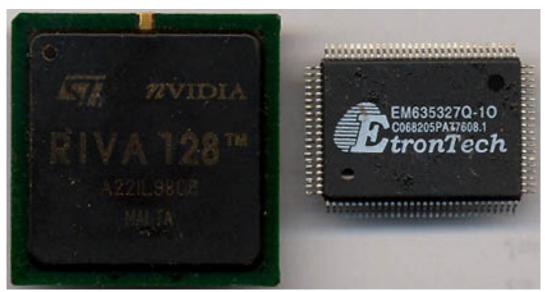
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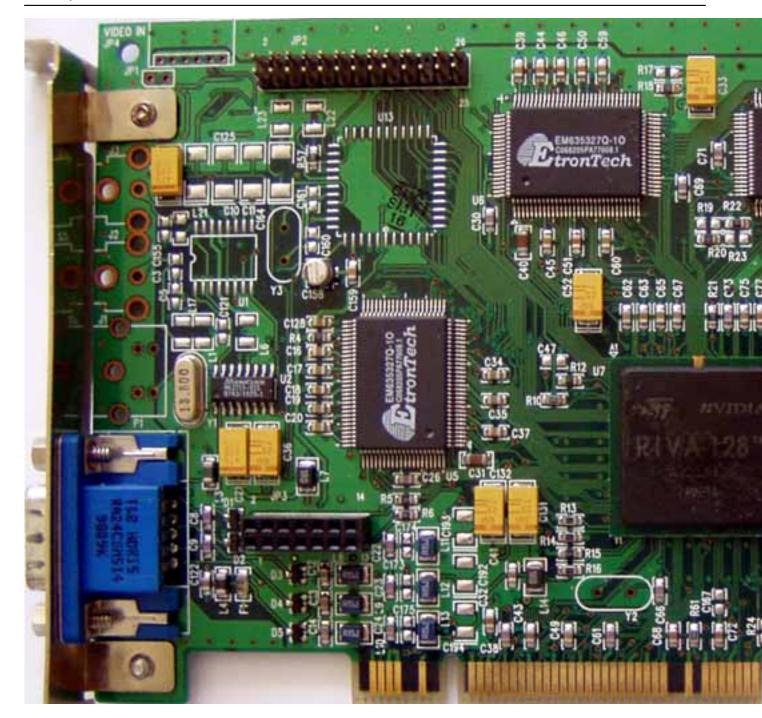
Core: NV3 100MHz 128bit

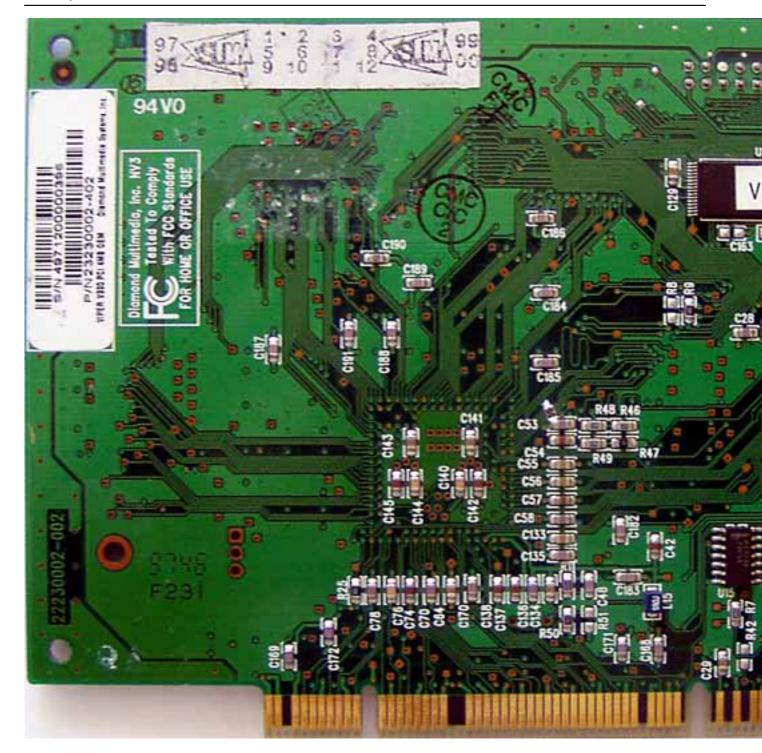
Memory: 4MB SGR 100MHz 128bit Year: 1997 Bus: PCI / AGP1x

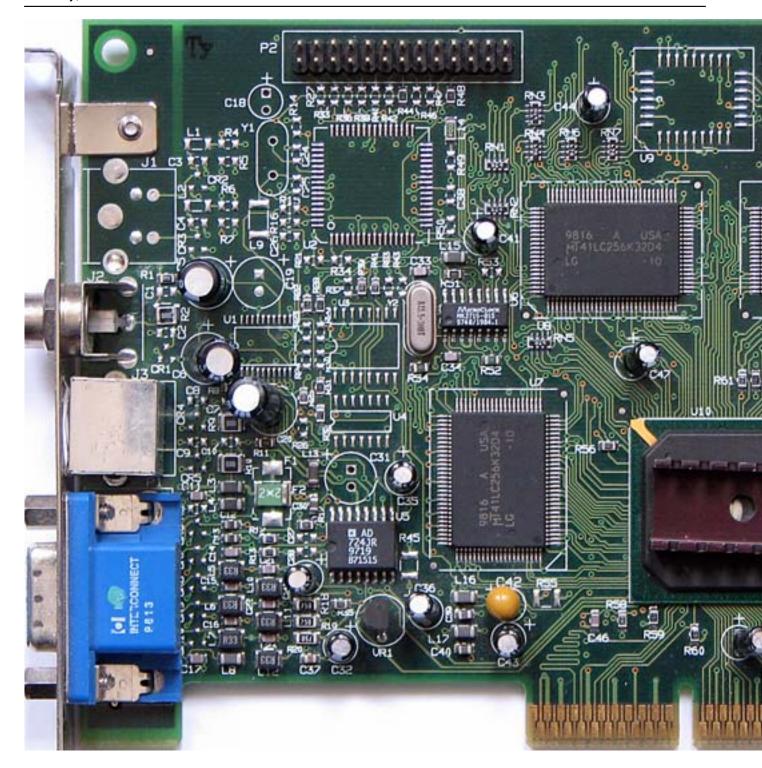
Made: SGS Thomson 350nm Malta Links: PDF datasheet Wikipedia Price: \$129

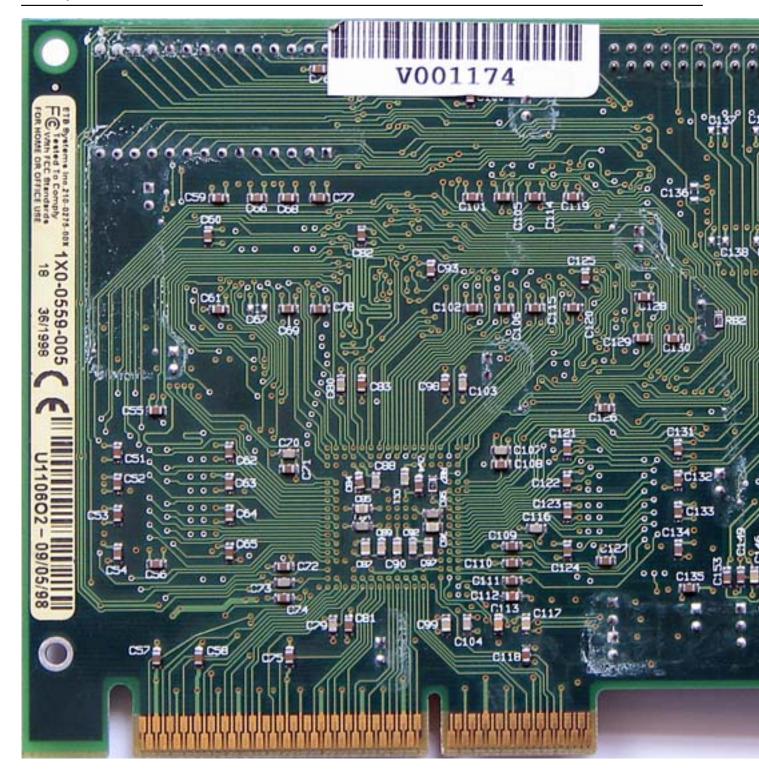
Bios: STB Velocity 128

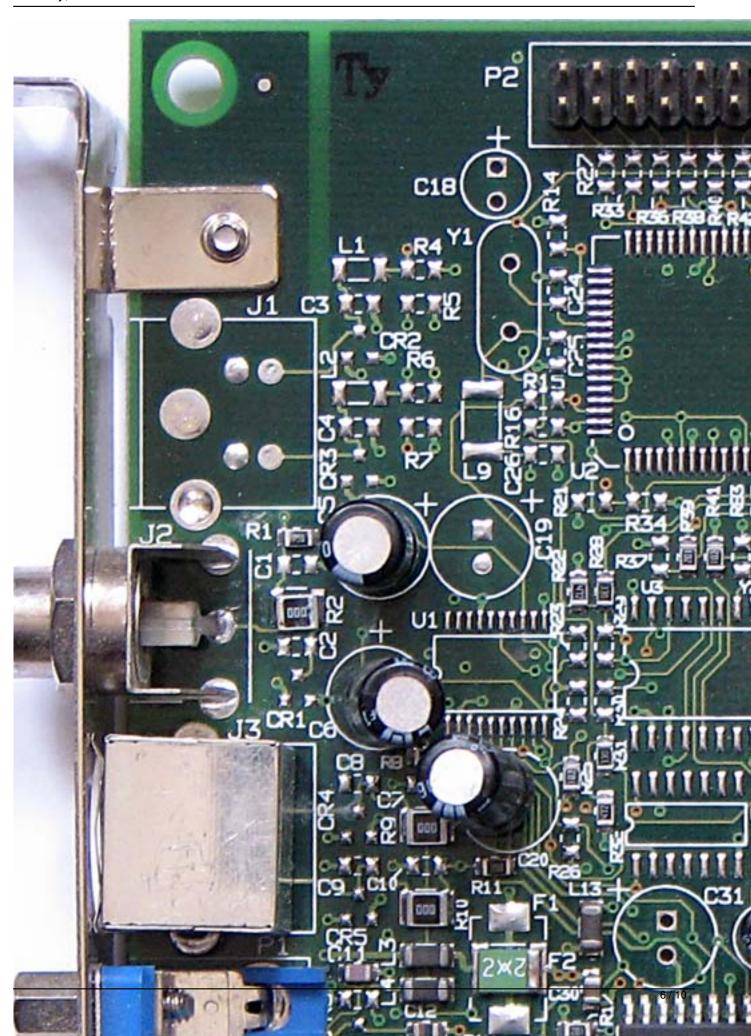
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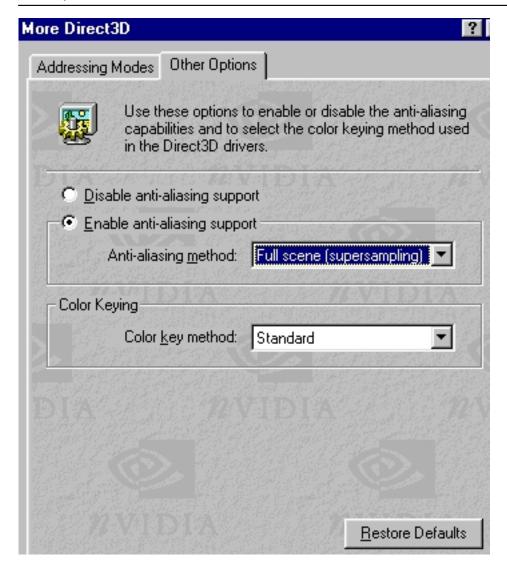






Addressing	Modes Other Options	2017AN U DATE CONTROL OF E	7.16.8889UN - 16
	Use these options to influence the texture coordinate system. Adjusting these settings can improve the visual quality of certain applications.		
Non-Filter	ed Texel Origin:		
	O Upper left corner (x =	0, y = 0)	
	C Center (x = texel/2, y	= texel/2)	
Minimum	<u> </u>		Maximum
<u>G</u> eometry	Extent Adjustment:	0,00 pixels	
Minimum			Maximum
n		<u>R</u> estore	Defaults

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