Written by palcal Saturday, 28 August 2010 20:07 - Last Updated Thursday, 23 February 2012 22:46



Core: NV5 133MHz 128bit

Memory: 16/32MB SDR/SGRAM 160MHz 128bit

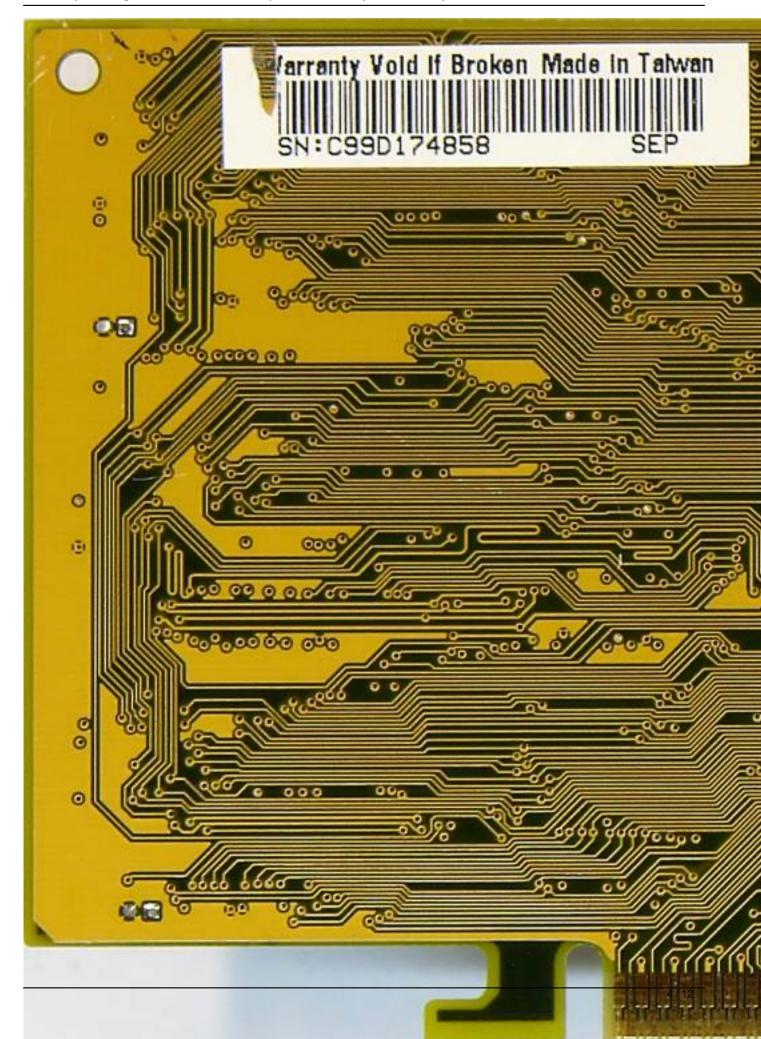
Year: 1999 **Bus:** AGP 4x

Made: TSMC 0,25 µm Taiwan Links: Techspot Anandtech Wikipedia

{webgallery}

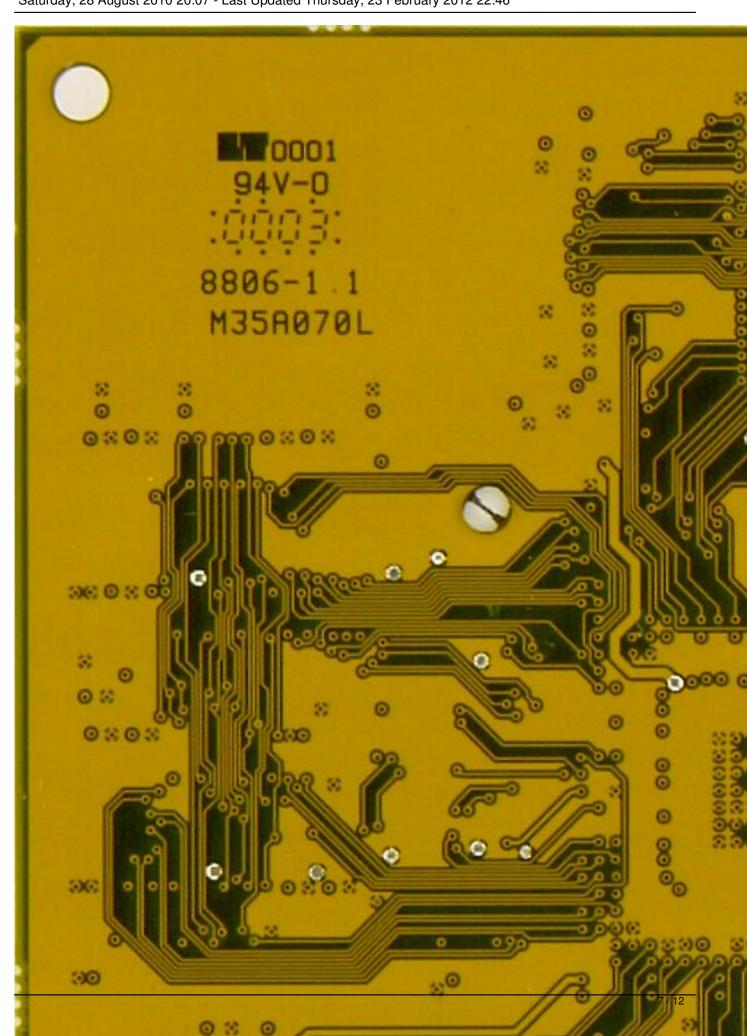


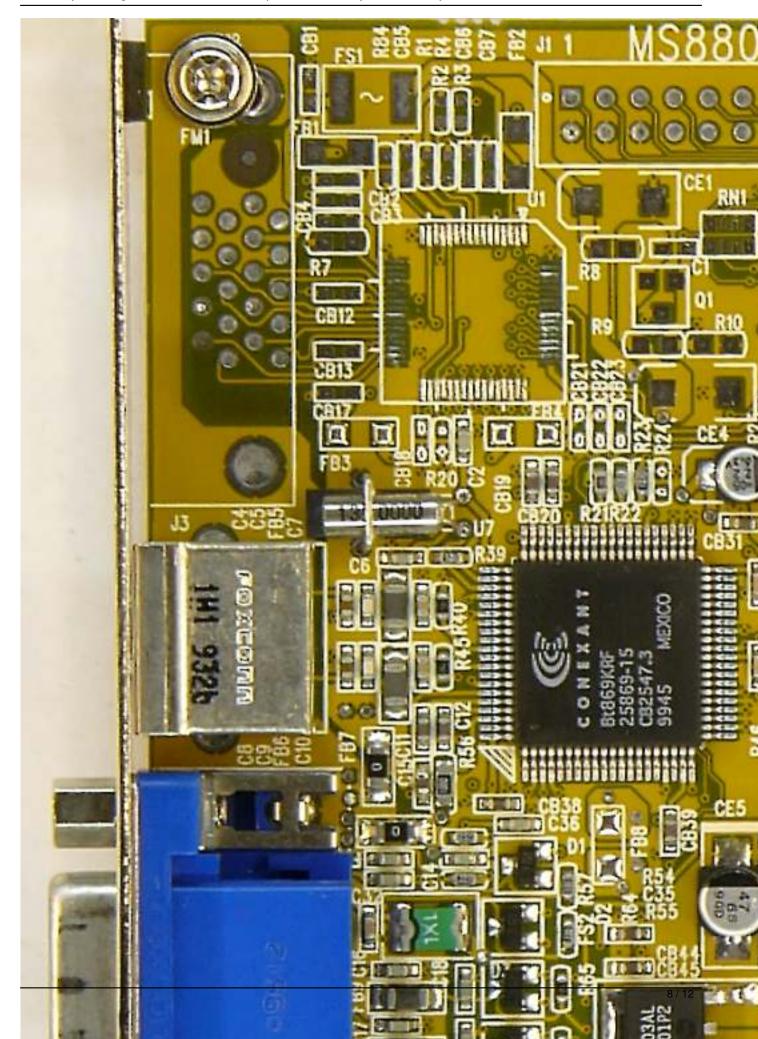
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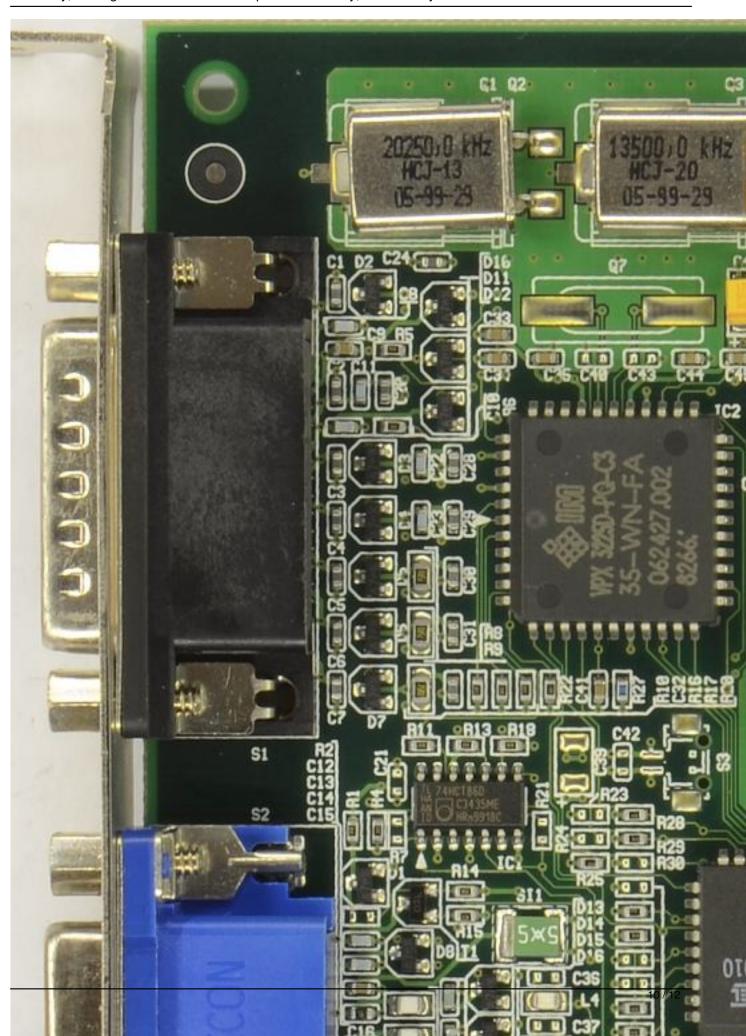








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