

ST Kyro II

Written by Zaatharen

Tuesday, 23 August 2011 23:49 - Last Updated Friday, 05 October 2012 23:46



Core: STG4500-X 175MHz 128bit **RAMDAC clock:** 300MHz

Memory: 64MB SDR 175MHz 128bit **Year:** 2001 **Bus:** AGP 2x **Made:** 180nm **Transistors:** 15 million

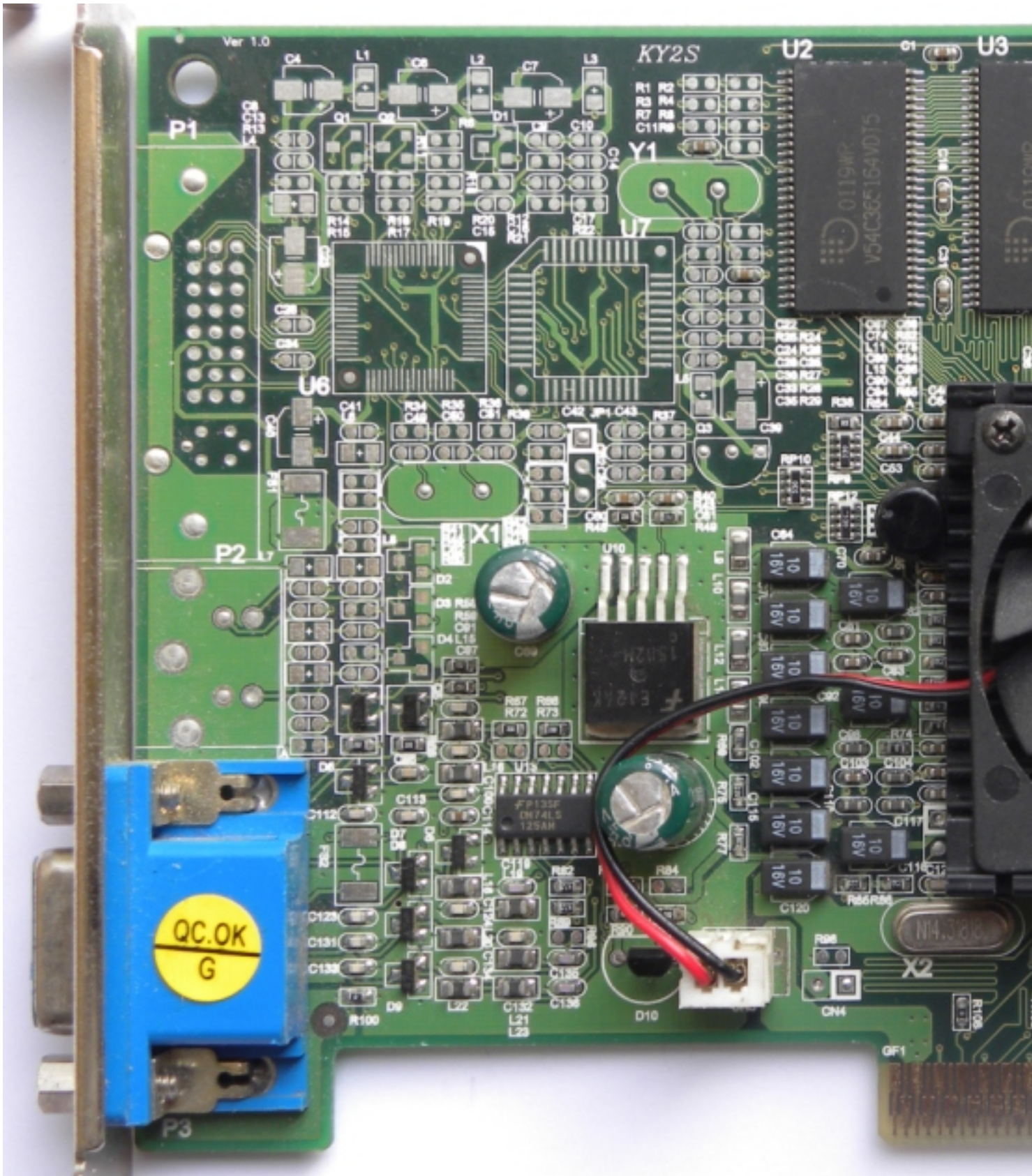
Pixel pipelines: 2 **TMU per pipeline:** 1 **Unified shaders:** 1 **DirectX:** 6

{webgallery}

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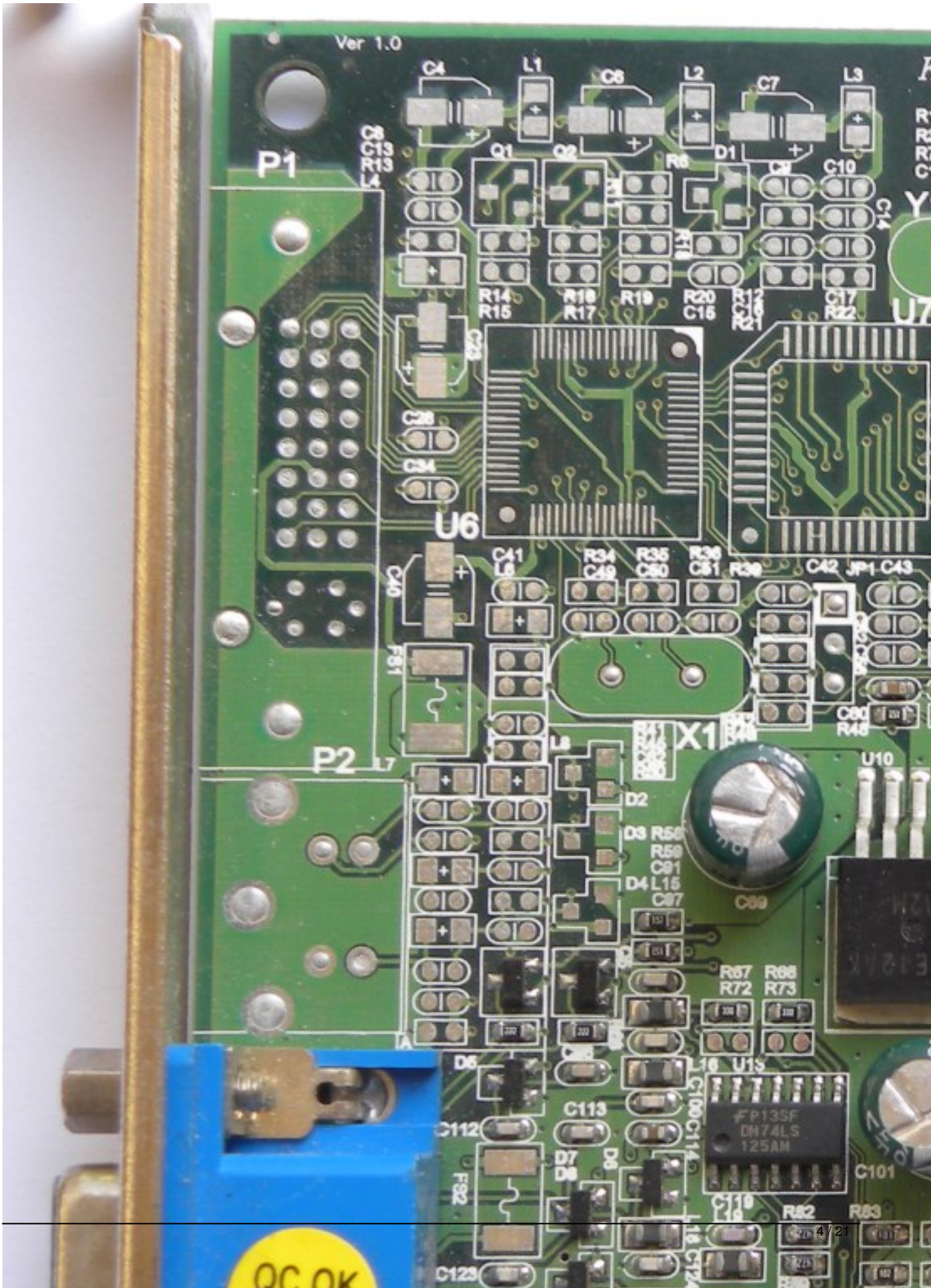
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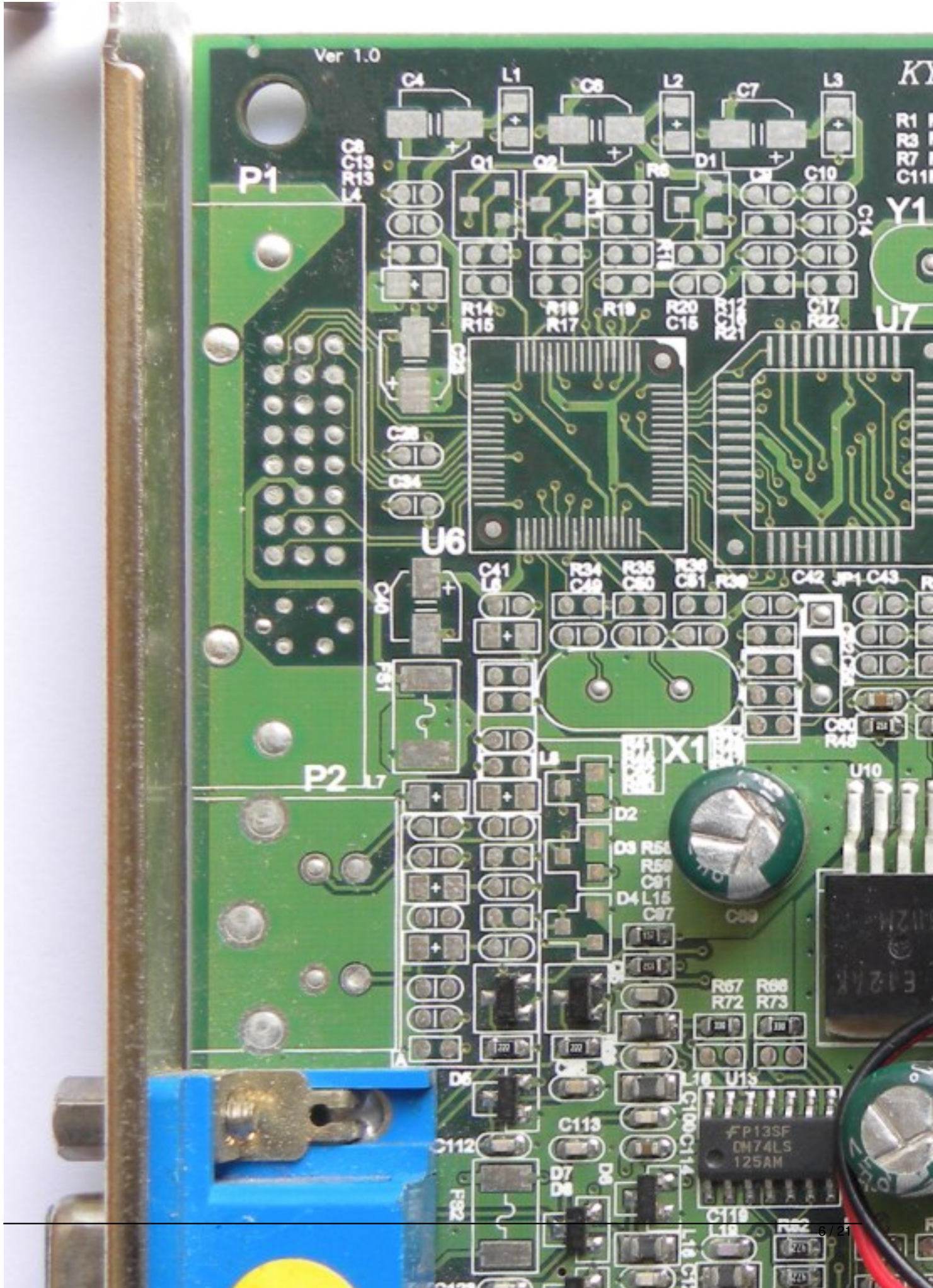
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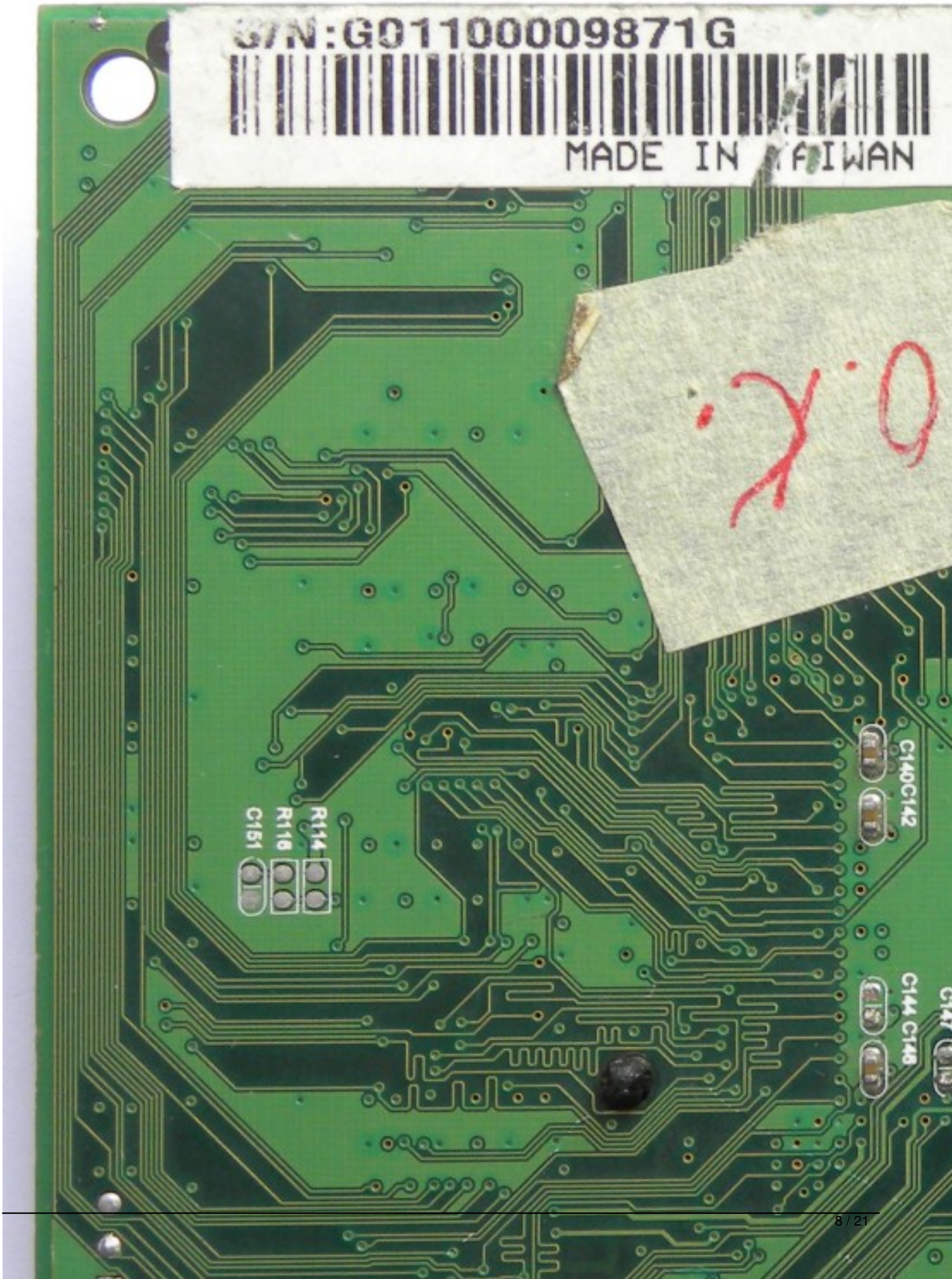
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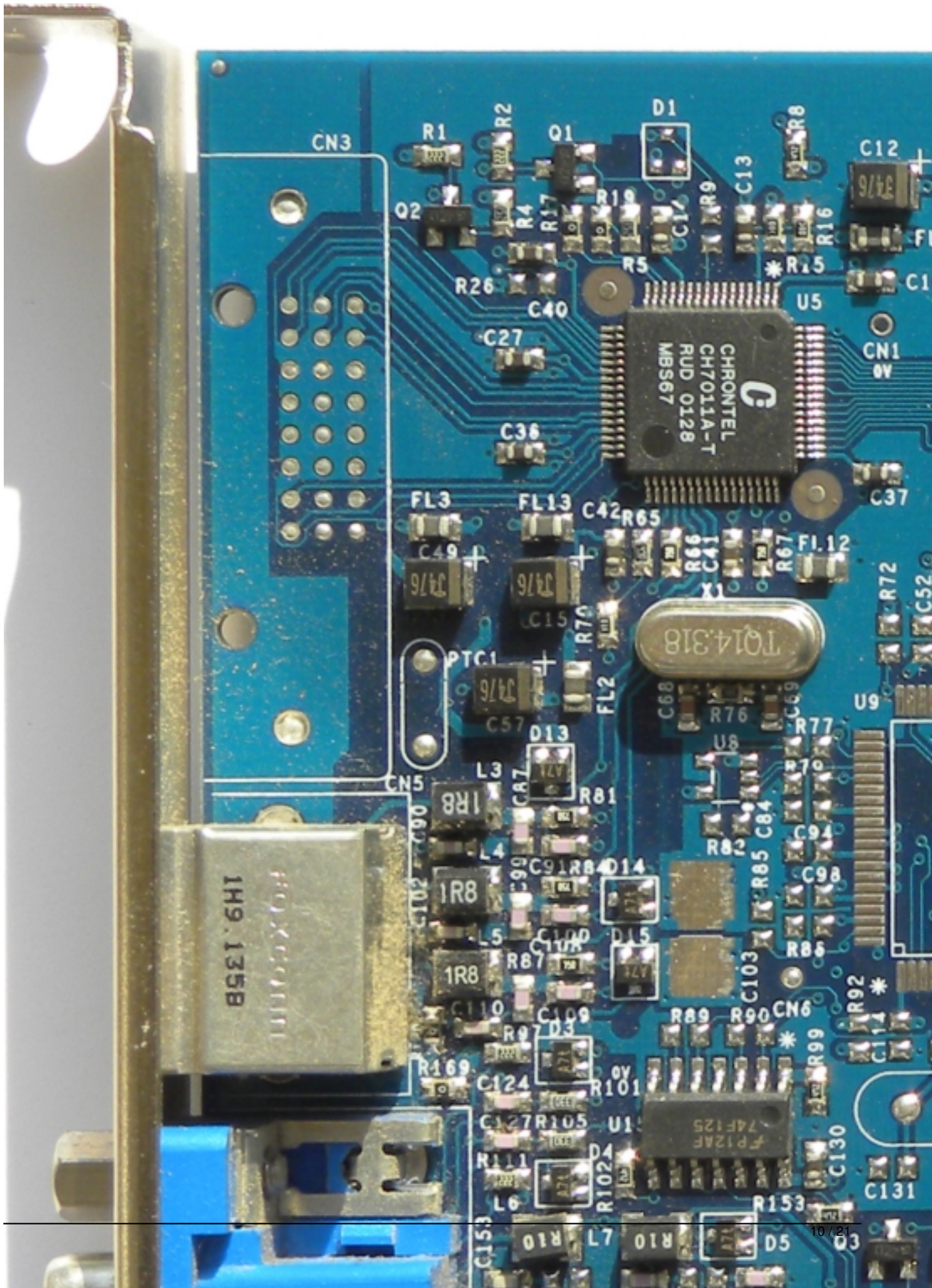
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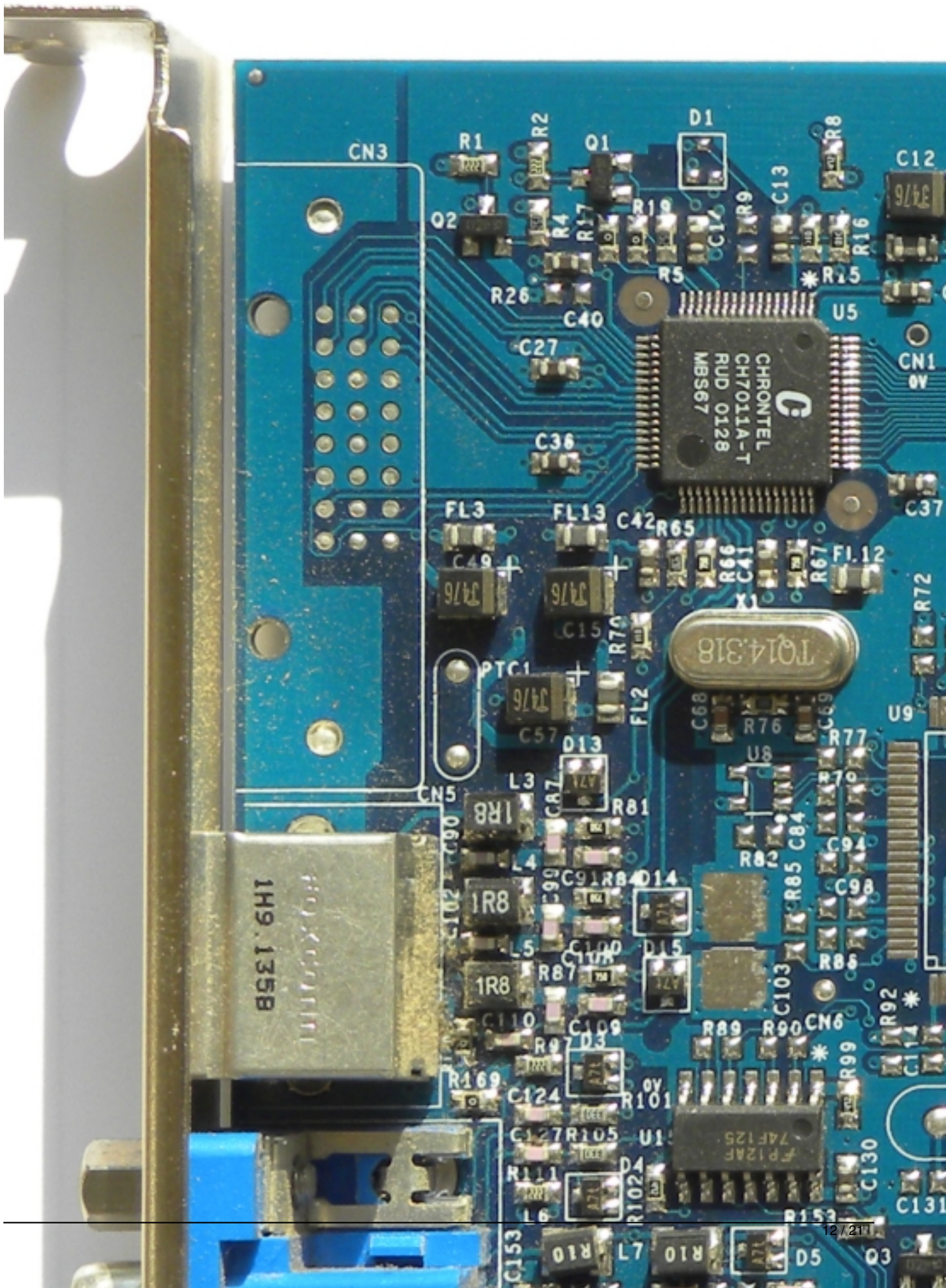
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Hercules®

BRAVE - záruční pečet



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HYUNDAI KOREA
HY57V653220B
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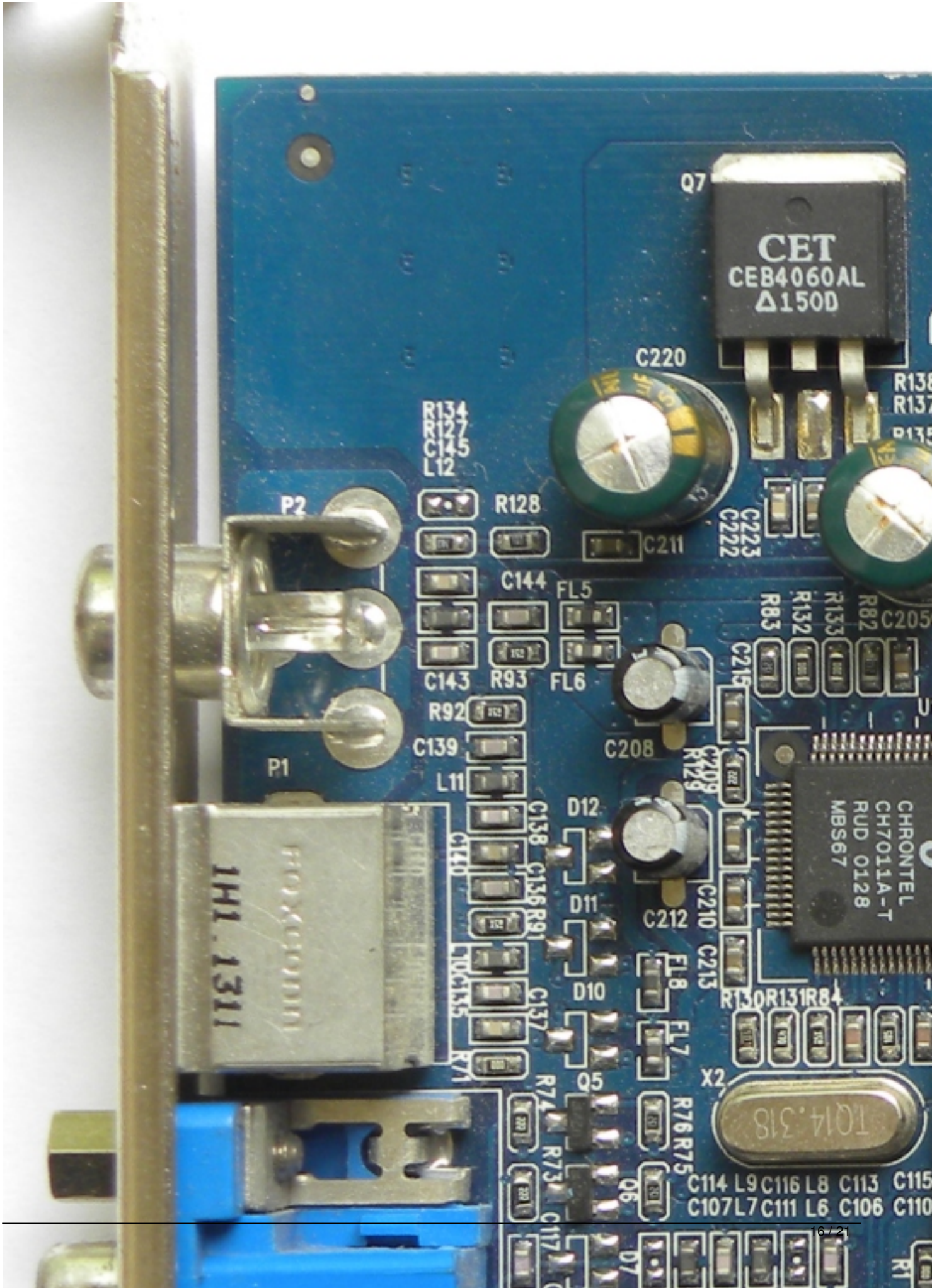
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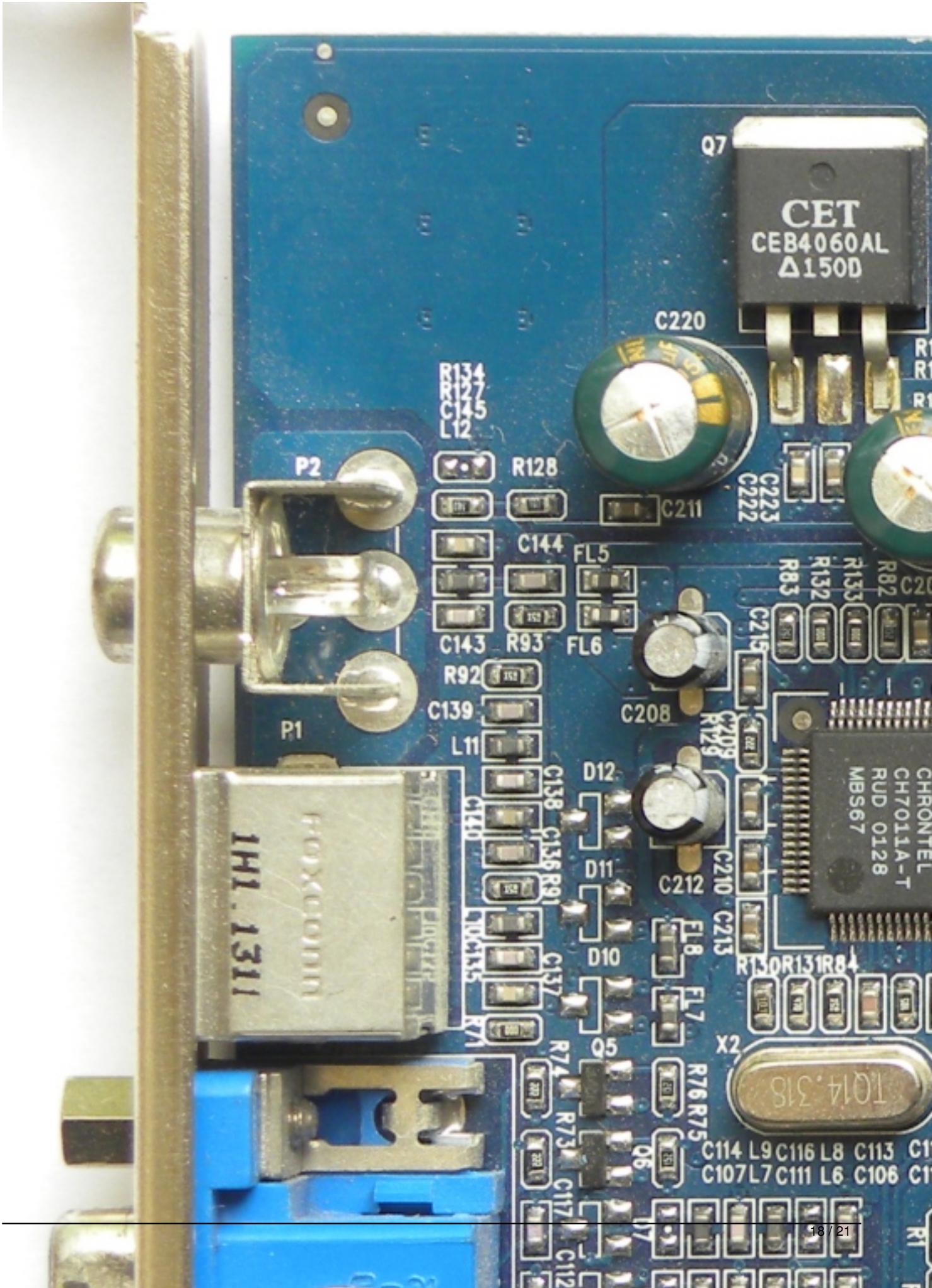
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~~Wanda's Darkening With Luminance~~
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