Written by Zaatharen Wednesday, 21 April 2010 22:10 - Last Updated Wednesday, 07 March 2012 14:35



Core: 86C365 100MHz 128bit Memory: 4MB SGR 100MHz 64bit

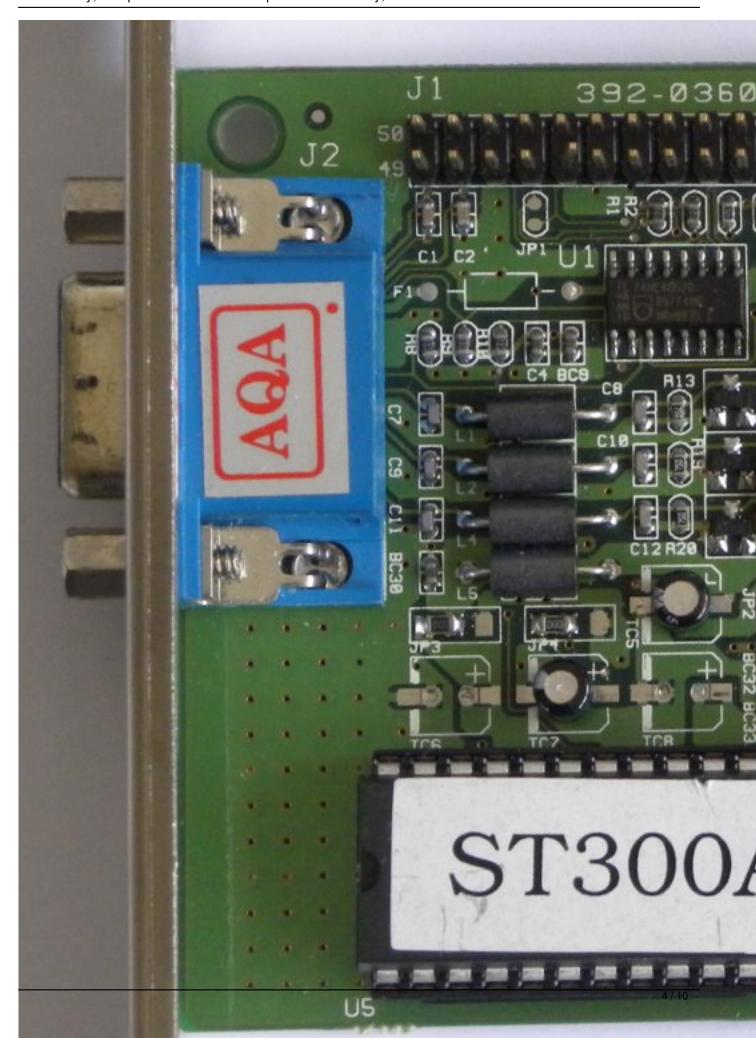
Year: 1997 **Bus:** AGP 1x

Pixel Pipelines: 1 TMU per pipeline: 1

{webgallery}



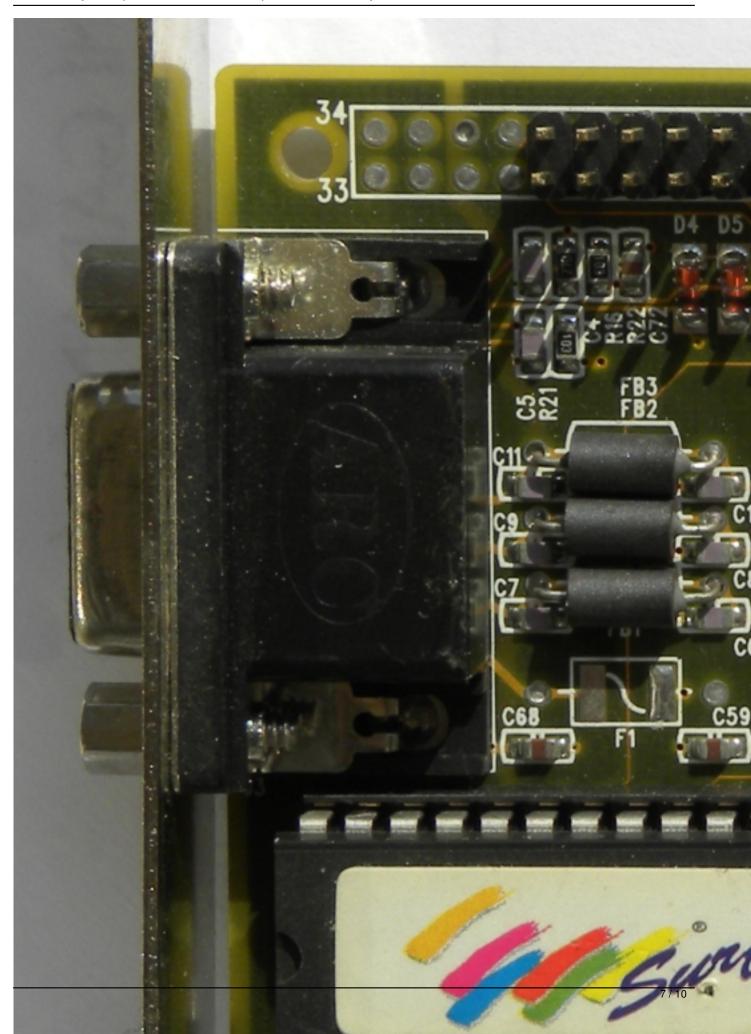






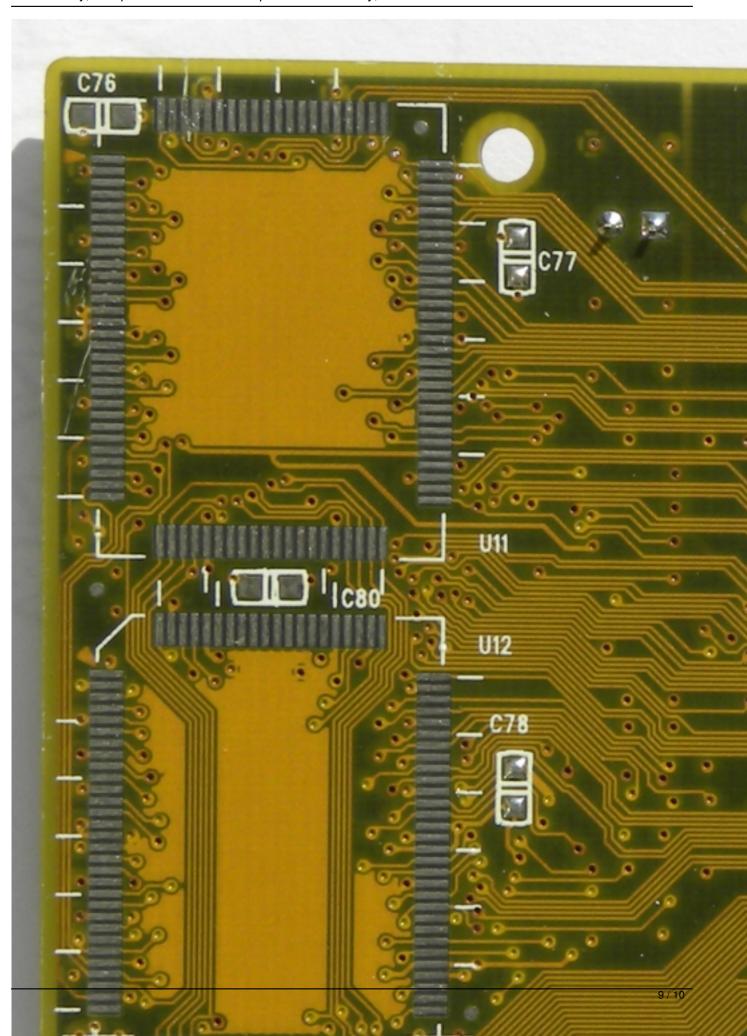
S3 Trio 3D

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{/webgallery}