Written by Sunday, 09 May 2010 16:04 - Last Updated Friday, 25 April 2014 11:28



Core: 86c325 50MHz 64bit Memory: 2/4MB EDO 50MHz 64bit Year: 1995 Bus: PCI Pixel pipelines: 1 TMU per pipeline: 1 Pix

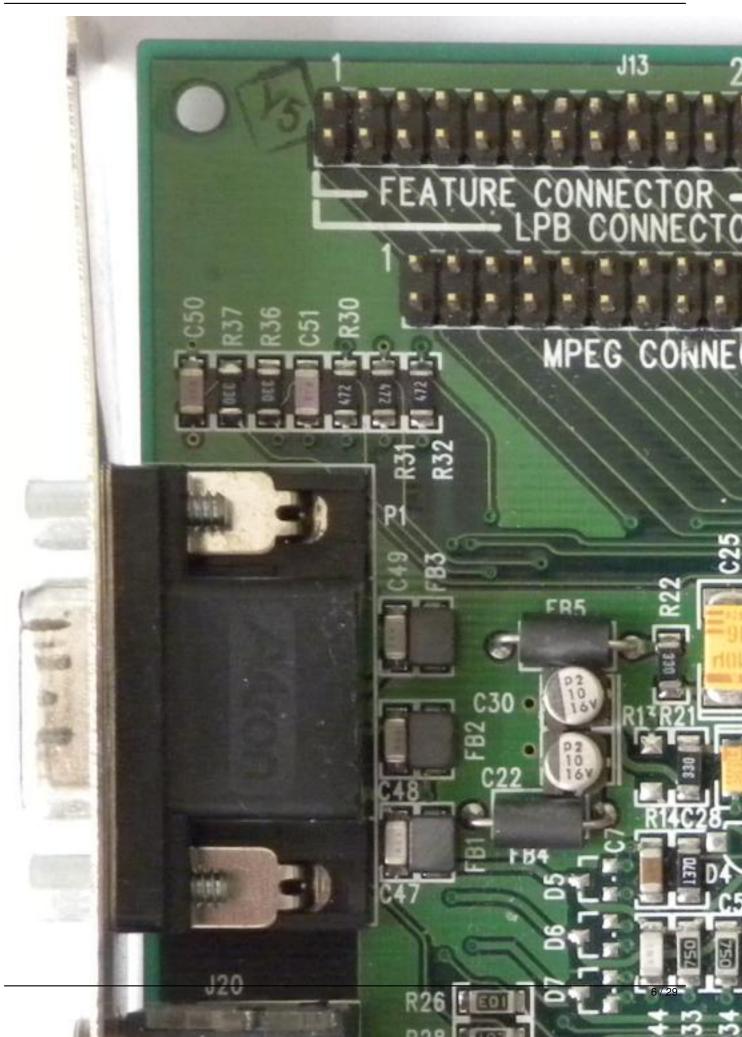
Pixel pipelines: 1 **TMU per pipeline:** 1 **Pixel write speed:** 50 MPixel/s**I Texel write speed:** 50 MTexel/s

{webgallery}

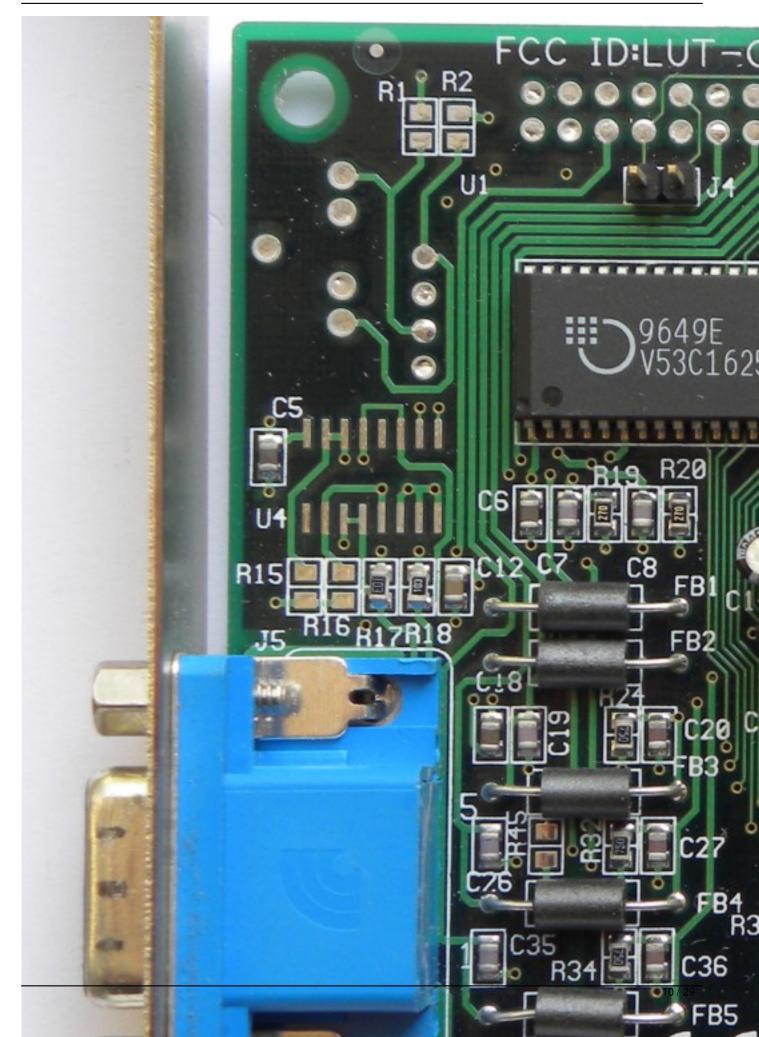
Written by Sunday, 09 May 2010 16:04 - Last Updated Friday, 25 April 2014 11:28



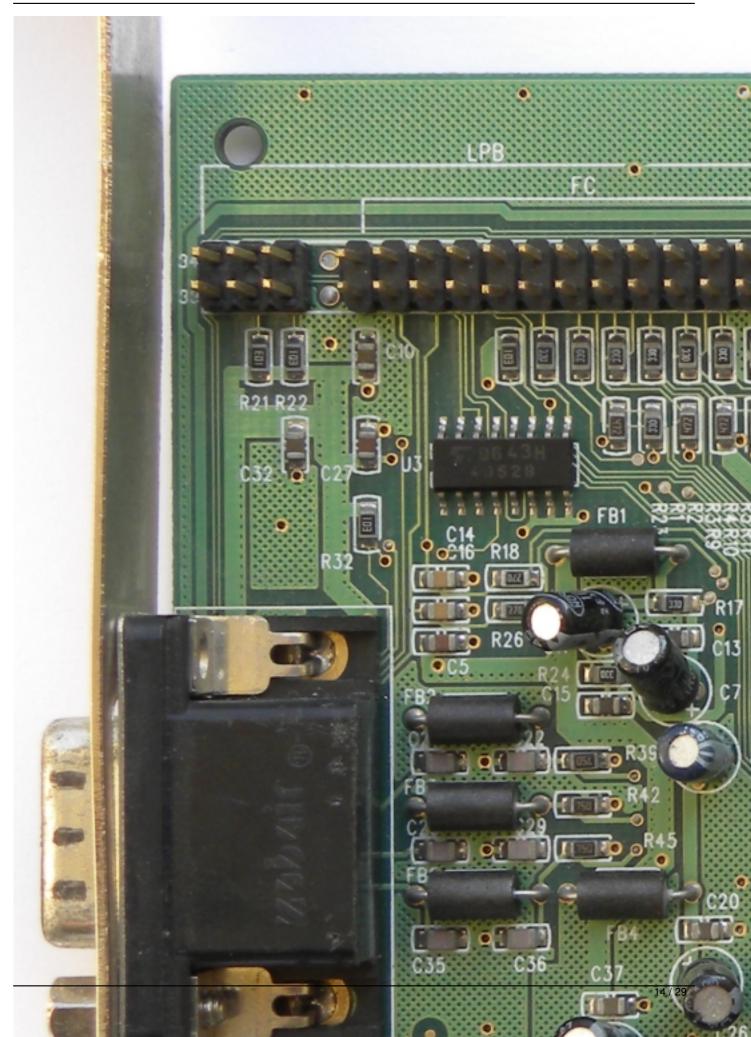


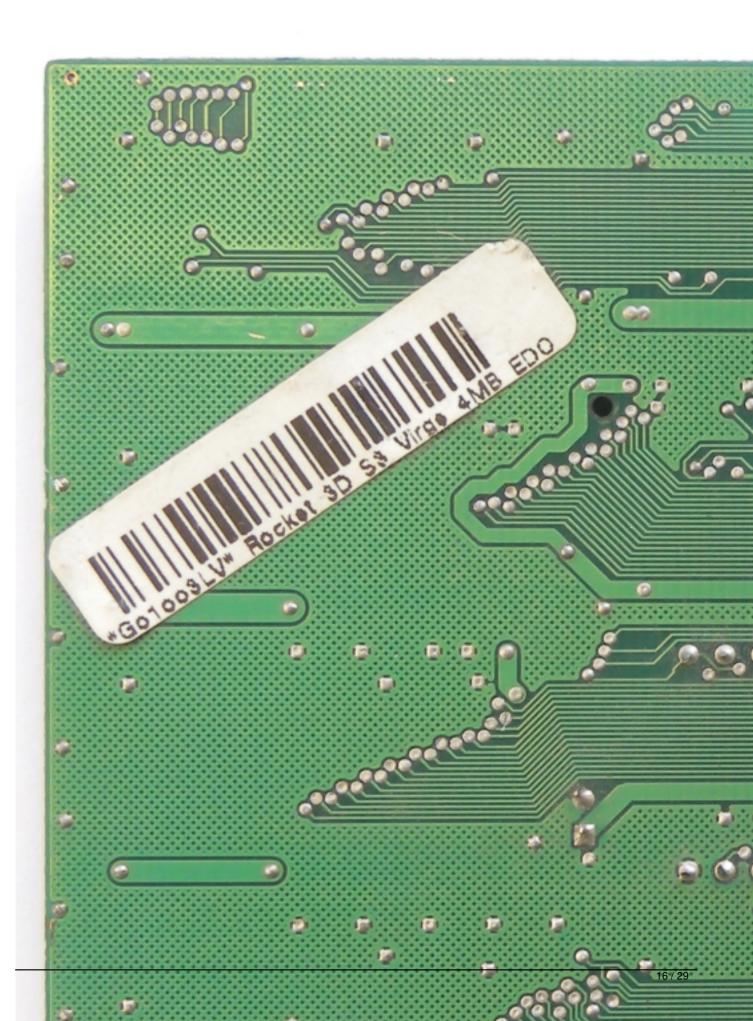


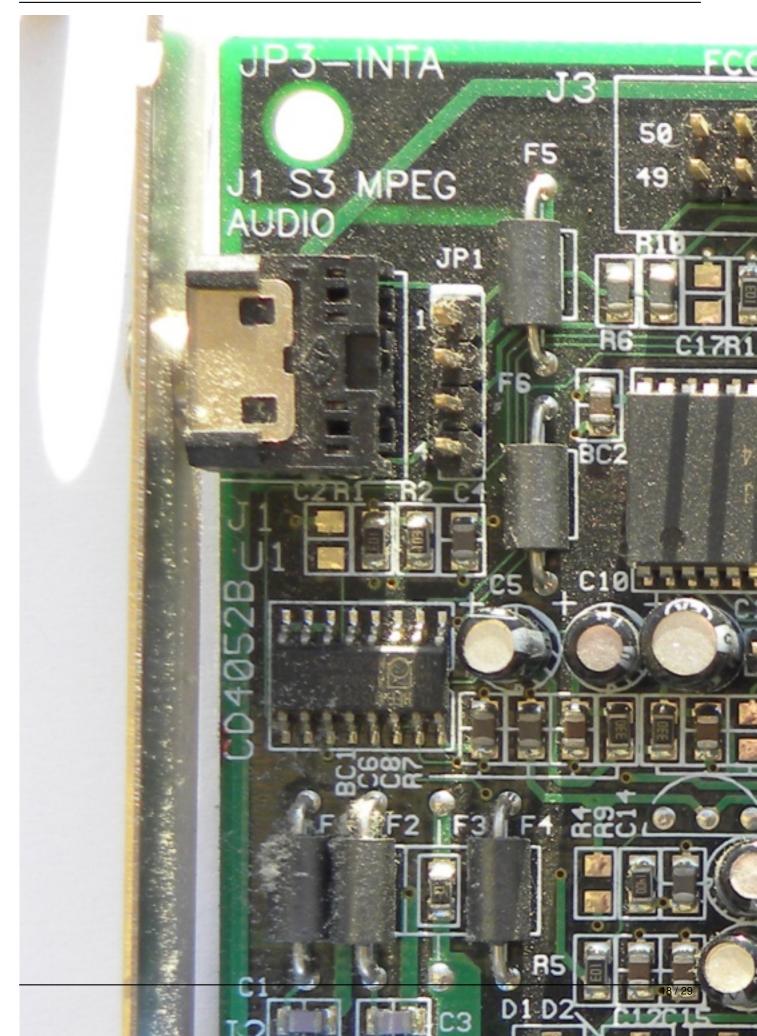


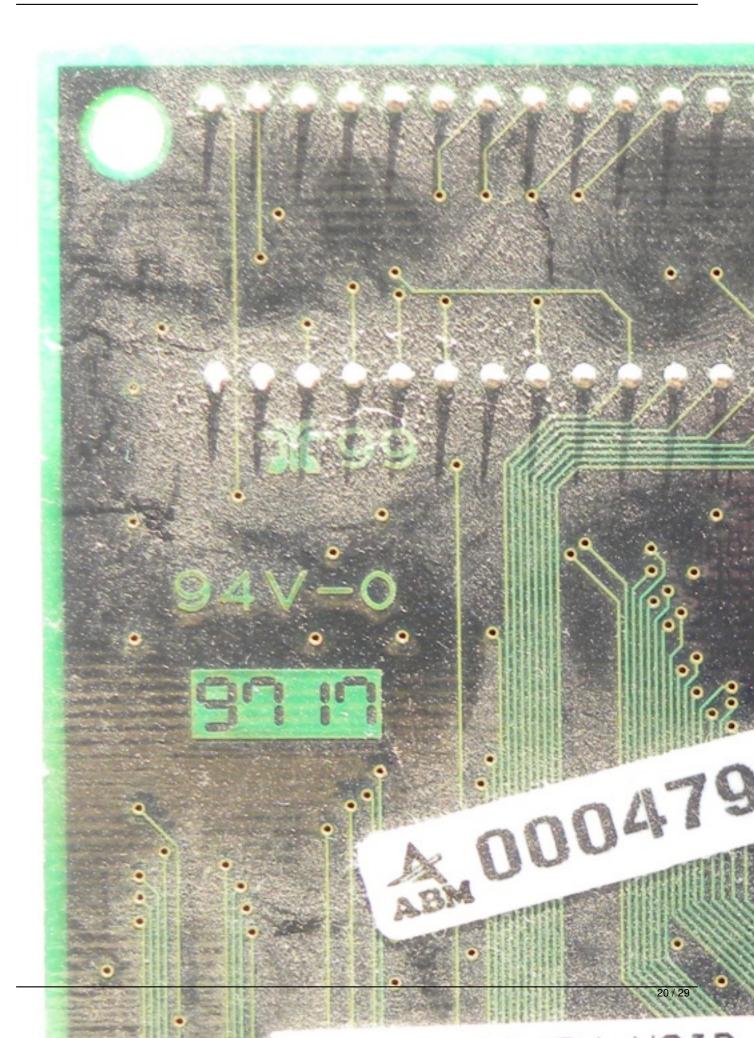


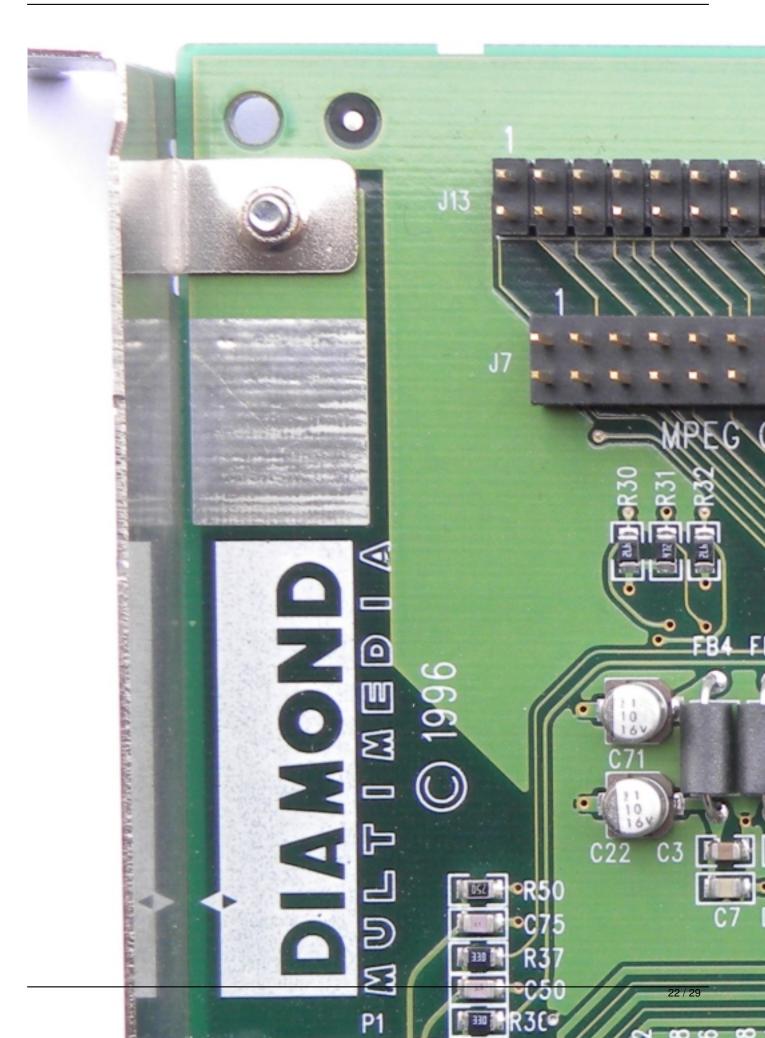


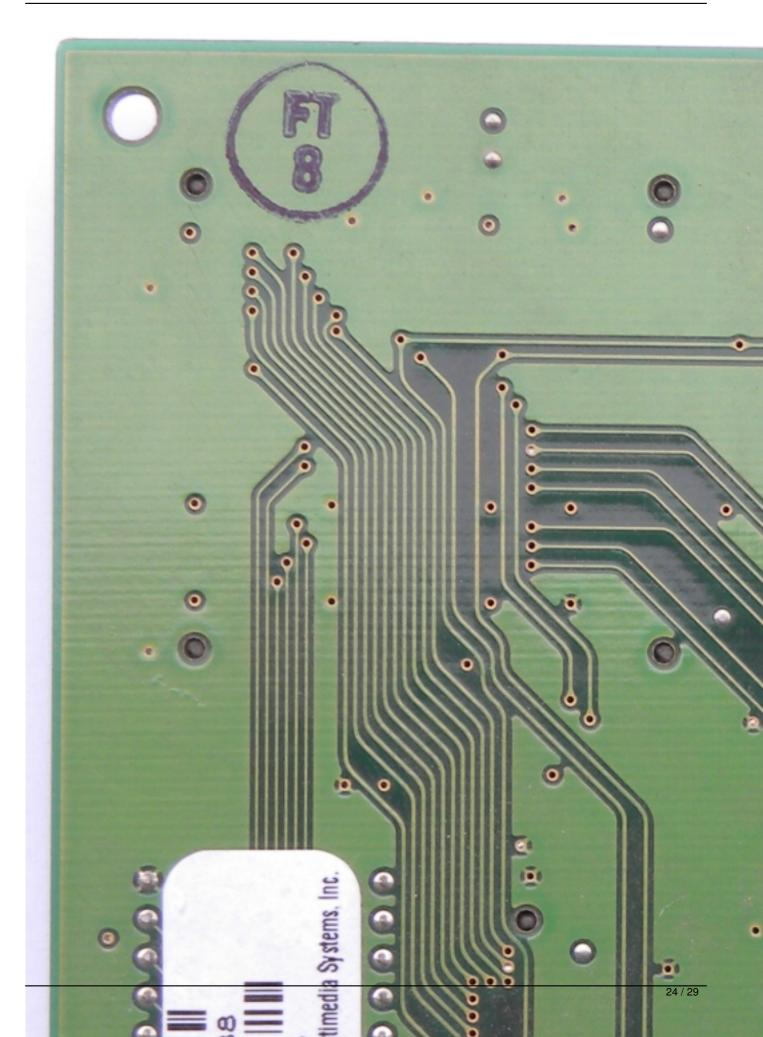


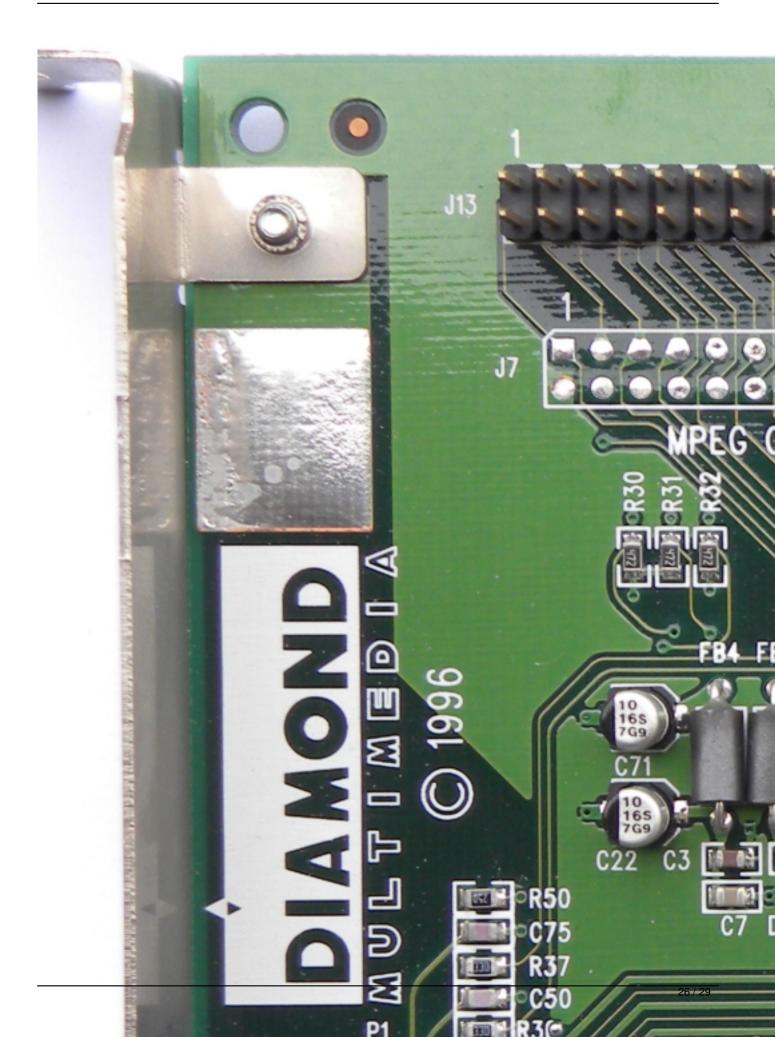














Written by Sunday, 09 May 2010 16:04 - Last Updated Friday, 25 April 2014 11:28

{/webgallery}