## S3 Trio 3D/2X

Written by Zaatharen
Saturday, 30 July 2011 20:02 - Last Updated Sunday, 18 March 2012 03:17


Core: 86C368 100MHz 64bit
Memory: 4/8MB SDR 100MHz 64bit Year: 1999 Bus: AGP 2x
Pixel pipelines: 1 TMU per pipeline: 1
\{webgallery\}

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