

Core: 305 Clock: 134MHz 128bit Memory: 16MB SDR 125MHz 64bit

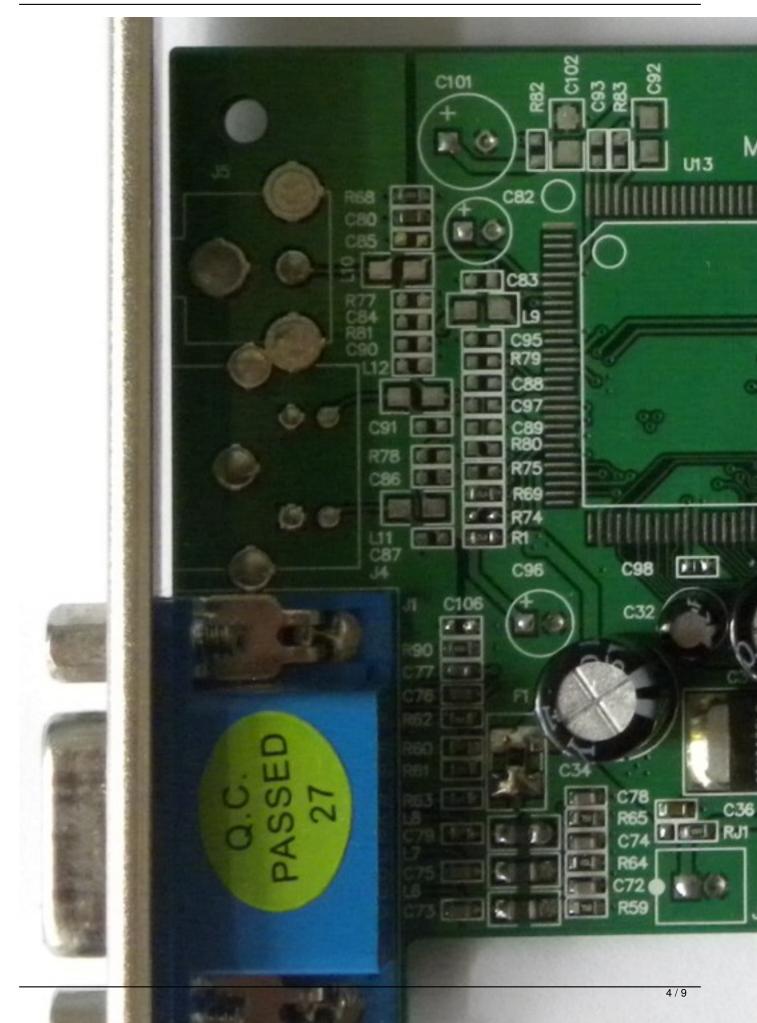
Year: 2000 Bus: PCI

Made: 0,22 μm Manufacturer: ST Lab Links: none known

{webgallery}







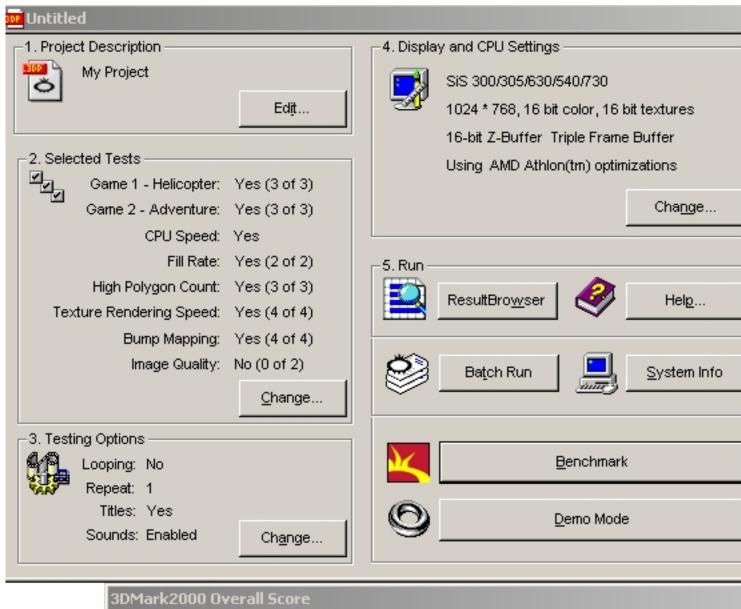
SiS 305

Written by Zaatharen Monday, 19 April 2010 07:26 - Last Updated Monday, 29 August 2011 22:54



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